What is claimed is:

1. A method of generating an initializing signal for initializing an inner circuit in a semiconductor memory device, the method comprising the steps of:

(a) receiving a mode set command for initializing the inner circuit; and

(b) generating a control signal in response to the received mode set command and using the control signal as the initializing signal.

2. The method of claim 1, wherein the mode set command is a signal applied to the semiconductor memory device via an external pin.

3. The method of claim 1, wherein the mode set command is a mode register set (MRS) command in a synchronous dynamic random access memory (DRAM).

4. The method of claim 1, wherein the mode set command is a Write Column address strobe (CAS) Before Row address strobe (RAS) (WCBR) in an asynchronous dynamic random access memory (DRAM).

5. A method of generating an initializing signal for initializing an inner circuit in a semiconductor memory device, the method comprising the steps of:

(a) receiving a precharge command for precharging the semiconductor memory device;

(b) receiving a mode set command for initializing the inner circuit after receipt of the precharge command; and

(c) generating a control signal in response to the received mode set command, and using the control signal as the initializing signal.

6. The method of claim 5, wherein the mode set command is a signal applied to the semiconductor memory device via an external pin.

7. The method of claim 5, wherein the mode set command is a mode register set (MRS) command in a synchronous dynamic random access memory (DRAM).

8.   The method of claim 5, wherein the mode set command is a Write Column address strobe (CAS) Before Row address strobe (RAS) (WCBR) in an asynchronous dynamic random access memory (DRAM).

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